

IN THE SPECIFICATION:

Page 4, in the Brief Description of the Drawings, replace the following descriptions with the following amended descriptions.

Figure 5 is an illustration of the system of Figure 4 connected to a computer interface for uploading and downloading data to the controller;

and

Figure 6 is a schematic illustration of the controller system underlying the invention;

Figure 7 is a perspective view of a jack plug having a metal can for bridging contacts in a patch port of Figure 2;

Figures 8a and 8b are diagrammatic illustrations of patch ports showing continuity detection circuitry for respective jack plugs at opposite ends of a patch cable;

Figure 9 is a flow chart of the method steps according to the first embodiment of the invention; and

Figure 10 is a flow chart of the method steps according to the second embodiment of the invention.

Page 5, replace the following paragraphs with the following amended paragraphs.

#### DISCUSSION

Figure 1 illustrates schematically a single jack (also called jack socket) of a structured cabling system. It will be appreciated that, in practice, many identical jacks will be present in such a system. For simplicity, all jacks of the system should preferably be in the form of the single jack illustrated in the drawing.

The jack comprises a conventional body 1 and contacts 2 in accordance with the RJ45 protocol. Adjacent the jack is provided a Light-Emitting Diode (LED) 3 which can be illuminated in response to a single signal 4 provided by a central processing unit. The LED 3 is immediately adjacent the jack 1 so that when the LED is illuminated, it identifies uniquely the jack to which it is adjacent.

The jack 1 is provided with two (split) partial shielding cans 5, 6 which, when no plug is present in the jack, are electrically isolated from each other. The two separate parts 5, 6 are connected by suitable cabling 7, 8 to a central processing unit.

Page 6, replace the following paragraph with the following amended paragraph.

Figure 2 shows two patch panels of the invention with a patch cable (lead) 12 extending therebetween~~[[7]]~~; Figures 3a, 3b, 3c respectively show a jacket socket, plug and plug positioned in jacket~~a jack~~ of the invention~~[[7]]~~; Figure 4 shows two patch panels with sockets according to the invention connected to a controller, Figure 5 shows the system of Figure 4 connected to a computer, and Figure 6 illustrates the controller system.

Figure 7 shows a perspective view of a jack plug 14 and a bridge contact 15. An electrical circuit is completed when the bridge contact of the jack plug is connected to two contacts at the sensor means in the jack socket. Figures 8a and 8b show electrical circuits connected to the two patch sockets to be connected by jack plugs at opposite ends of a patch cable, a detector circuit 16 being shown for one jack socket and a detector circuit 17 being shown for the other jack socket. These detector circuits, combined with the controller (see Fig. 4) to which they are connected, provide continuity checking means.